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ASX RELEASE 16 October 2025

ASX: NVU

Ignite Investment Summit Hong Kong Presentation

Nanoveu Limited (ASX: NVU, OTCQB: NNVUF) ("Nanoveu" or the "Company"), a technology innovator across advanced semiconductor, visualisation, and materials science, is pleased to advise of its participation at the Ignite Investment Summit being held this week in Hong Kong.

Nanoveu's Chief Executive Officer of Semiconductor Technologies, Mr Mark Goranson, will be presenting the Company's strategy for progressing its EMASS semiconductor division today at 12.00 pm AWST. Attached is the presentation that Mr Goranson will be speaking to at the conference.

This announcement has been authorised for release by the Board of Directors.

-ENDS-

Nanoveu Media Alfred Chong, Nanoveu MD and CEO P: +65 6557 0155

E: info@nanoveu.com

About Nanoveu Limited

Further details on the Company can be found at https://nanoveu.com/.

EMASS is a pioneering technology company specialising in the design and development of advanced systems-on-chip (SoC) solutions. These SoCs enable ultra-low-power, Al-driven processing for smart devices, IoT applications, and 3D content transformation. With its industry-leading technology, EMASS will enhance Nanoveu's portfolio, empowering a wide range of industries with efficient, scalable AI capabilities, further positioning Nanoveu as a key player in the rapidly growing 3D content, AI and edge computing markets.

EyeFly3D™ is a comprehensive platform solution for delivering glasses-free 3D experiences across a range of devices and industries. At its core, EyeFly3D™ combines advanced screen technology, sophisticated software for content processing, and now, with the integration of EMASS's ultra-low-power SoC, powerful hardware.

NanoshieldTM is a self-disinfecting film that uses a patented polymer of embedded Cuprous nanoparticles to provide antiviral and antimicrobial protection for a range of applications, from mobile covers to industrial surfaces. Applications include $Nanoshield^{TM}$ Marine, which prevents the growth of aquatic organisms on submerged surfaces like ship hulls, and $Nanoshield^{TM}$ Solar, designed to prevent surface debris on solar panels, thereby maintaining optimal power output.

Forward Looking Statements This announcement contains 'forward-looking information' that is based on the Company's expectations, estimates and projections as of the date on which the statements were made. This forward-looking information includes, among other things, statements with respect to the Company's business strategy, plans, development, objectives, performance, outlook, growth, cash flow, projections, targets and expectations and related expenses. Generally, this forward-looking information can be identified by the use of forward-looking terminology such as 'outlook', 'ambition', 'anticipate', 'project', 'target', 'potential', 'likely', 'believe', 'estimate', 'expect', 'intend', 'may', 'mission', 'would', 'could', 'scheduled', 'will', 'plan', 'forecast', 'evolve' and similar expressions. Persons reading this announcement are cautioned that such statements are only predictions, and that the Company's actual future results or performance may be materially different. Forward-looking information is subject to known and unknown risks, uncertainties and other factors that may cause the Company's actual results, level of activity, performance, or achievements to be materially different from those expressed or implied by such forward looking information.







IGNITE Investment Summit Presentation 2025





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ACCEPTANCE By attending a presentation or briefing, or accepting, accessing or reviewing this document you acknowledge, accept and agree to the matters set out above.

AUTHORISATION This document has been authorised for release by the Company's Board of Directors.



Capital Structure



Capital Structure*

ASX Code	NVU	
OTCQB Code	NNVUF	4
Shares on Issue	981.1m	
Options on Issue	228.5m	
Average Volume	13.6m	
Market Cap	\$85.4m	
Previous Close	\$0.087m	



Board and Management



Dr. David PevcicExecutive Chairman

- Experienced professional and investor in the resources and technology sector
- Non-Executive Chairman at Battery Age Minerals Ltd (ASX: BM8).
- Non-Executive Chairman at Infini Resources Ltd (ASX: 188).
- Holds a Bsc, MBBS, from the university of Western Australia.



Alfred Chong Group CEO and Director

- Founder of Nanoveu, has 30+ years of experience in scaling companies and trade sales
- Former CEO of: Atex Media Command (APAC), THISS Technologies, 121View
- Former CMO at 3D International



Michael Winlo

Non-Executive Director

- Former CEO of Linear Clinical Research.
- Executive Director at Emyria Ltd (ASX:EMD)
- Former Health Lead at Palantir (NYSE:PLTR).
- · Holds an MBA from Stanford and an MBBS from UWA.



Steve Apedaile

Non-Executive Director

- 30 years of experience in accounting
- · Worked at KPMG and Horwath Hong Kong.
- Fellow of the ICAEW.
- Member of the AICD.
- Executive Chairman of Sprintex (ASX:SIX).







Semiconductor Leadership Team









Mark Goranson

CEO of Semiconductor Technology

- VP of Global Ops, TE Connectivity
- SVP of Fab Ops, ON Semi
- VP of Fab Ops, Freescale
- Early Member of Intel

Dr. Mohamed Sabry

CTO, Founder of EMASS

- Associate Professor, NTU Singapore
- Postdoc, Stanford
- Recipient of Nanyang Education Award
- Ph.D. from EPFL

Scott Smyser

VP, Sales & Marketing

- EVP Marketing & BD, Si-Ware Systems
- VP & GM, VTI Technologies (Murata)
- SVP Sales, Atomica
- SVP Strategic Sales, Rockley Photonics







EMASS Introduction

Fabless Semiconductor Innovator in Edge AI Processing



- Ultra-low-power Edge AI SoCs for always-on intelligence in battery constrained devices
- Flagship ECS-DoT chip up to 20x more energy efficient than peers
- Usable across drones, wearables, IoT devices and other Edge Al applications

Established Global Operations



- Global operations and R&D center's in USA, Singapore and Egypt
- 100% of EMASS acquired by Nanoveu Limited (ASX:NVU), March 2025

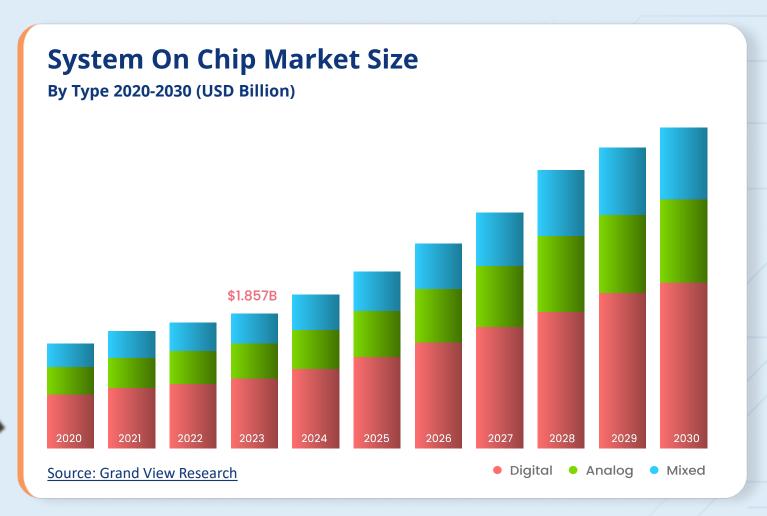
Expert Team Across Disciplines



- Deep expertise in AI/ML, neural network acceleration, semiconductor design, sensor fusion, and embedded systems
- Experienced management with semiconductor and AI industry backgrounds



Semiconductor & SoC Market Opportunity



SoC Market Growth:

Applications demanding continuous sensing, context awareness, and real-time decision-making

\$325.7B **by 2030** 8.5% Global Market CAGR

Driven by AI, 5G, and smart devices.





Revolutionising Edge Al



Edge AI Opportunities



Robust AI models require increased computation power and battery



Current chips are not designed for efficient edge Al use cases



Devices need versatile chips with multi-sensor use-cases



Edge AI Solution



Executes AI inference directly at the sensor under 1 mW power



Provides up to 20x energy efficiency vs leading chips



Enables smaller, lower-cost, and reconfigurable designs across drones, IoT, and wearables





Introducing the ECS-DoT

Superior Performance, Low Power, Small Form Factor



Up to 12 TOPs/Watt



Compressed Al Model < 2-bits



4 MBytes



22nm technology with 7mm²die area



30 GOPs @ 50 MHz, 2mW





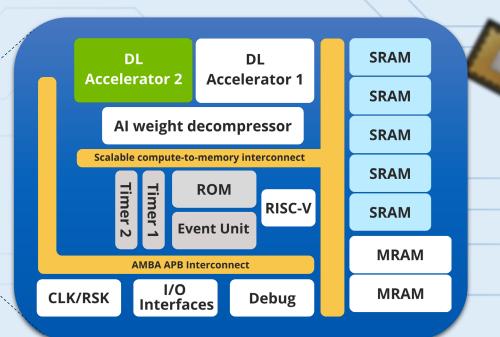
Redefining What's Possible in Ultra-Low-Power Edge Al

Fully Programmable System on Chip

- Processor
- Al engines
- IPs for compressed Al

Local decision-making at the Edge

- Always-on Al Inference
- Sensor Fusion
- Support CNNs, ML, Data Processing etc.



Key Architecture Features

- <1 to 5 mW power consumption (Avg 2mW always on)
- No External DRAM needed (very fast on-chip SRAM and nonvolatile MRAM/RRAM)
- SW and HW support for highly compressed AI workloads

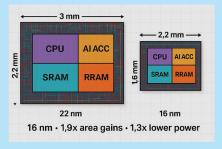




Next Generation ECS-DoTTape-out of Advanced 16nm Architecture

Built on TSMC's 16nm Fin Field Effect Transistor (FinFET)

- Higher Perf. & Lower Power
- Smaller Die Size
- Production Ready



Integrating Wireless Connectivity







Bluetooth 5.x + Bluetooth Mesh

Wi-fi

Lora for low-power, long-range (optional)

Key architecture enhancements

- Single/Dual-core
- Improved AI performance with dynamic 1-16 bit precision
- Expanded support for AI operators: CNNs, Transformers, NLP, object detection

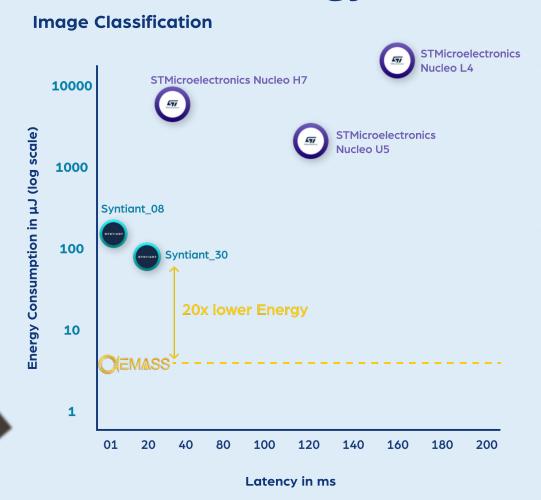
Building for scalable edge AI deployment

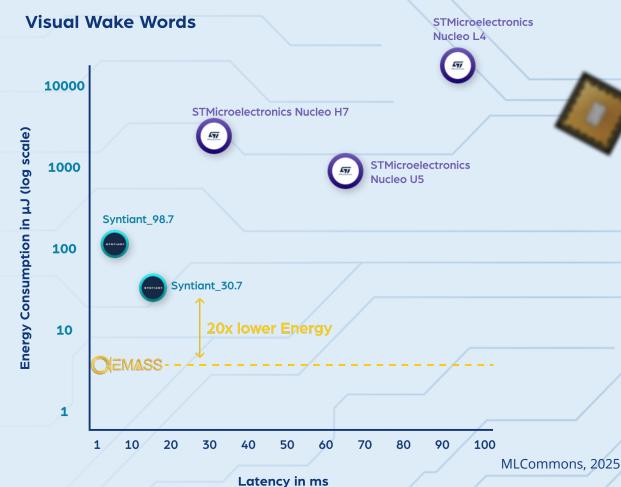
- Edge autonomy: local sensing, AI processing, and wireless communication all on one chip
- System cost reduction: fewer external components, smaller PCB
- Scalable platform for wearables, smart home, drones, industrial sensing, and more





Exceptional AI Computation 20X Lower Energy, Same Performance







ECS-DoT Differentiation Full AI capability, true always-on operation

Key Attribute	ECS-DoT	Typical Alternatives
Compute Architecture	RISC-V + Dual Deep Learning Accelerators	General MCU, DSP, or fixed-function NNs
Memory Architecture	Fully on-chip (up to 8MB) — No external DRAM	External DRAM often required
Model Efficiency	Compressed models (~1.3 bits/weight)	Full-precision models, higher memory demand
Power Consumption	~2mW active power	10–50mW or higher
Always-On Performance	Optimized for continuous inference	Not designed for continuous sensing
Integration Complexity	Single-chip, minimal BOM	Complex multi-chip solutions
Supported Workloads	Neural networks + classical + sensor fusion	Often limited to specific NN types
Package Size	5mm x 5mm QFN-40	Often larger footprints
Target Applications	Audio, vision, sensor fusion, IoT, XR, wearables	More narrow or general-purpose





Target Edge Al Sectors



\$USD 138.5B by 2029

Source: Marketsandmarkets



\$USD 368.4B by 2035

Source: Fact.MR

Al Cameras

\$USD 22.1B by 2028

Source: Marketsandmarkets



IoT Sensory

\$USD 168.69B by 2030

Source: Mordor Intelligence



AR Glasses

\$USD 8.26B by 2030

Source: Grand View Reasearch



Drones

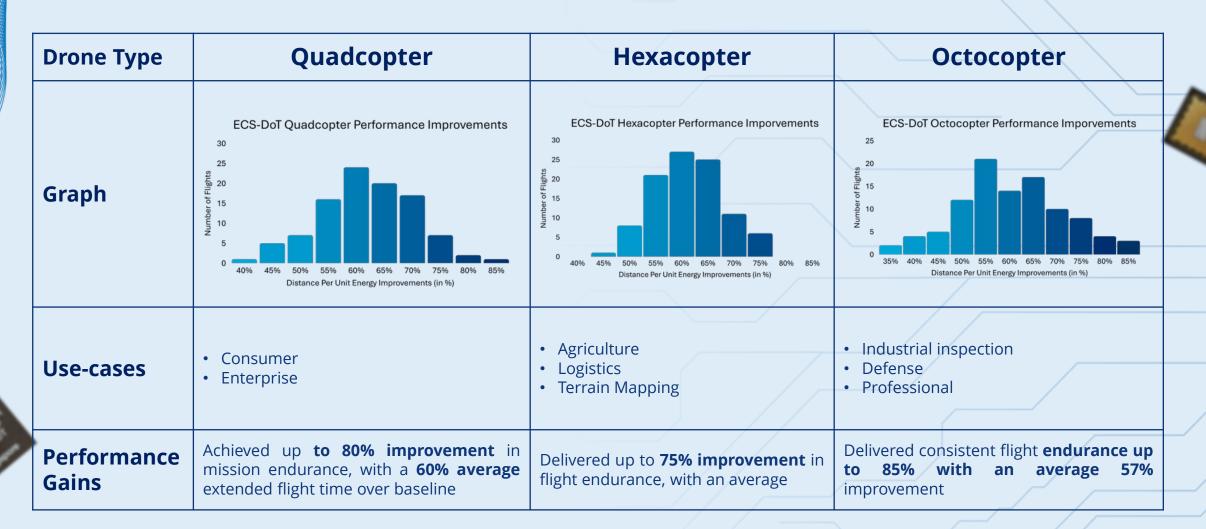
\$USD 223.66B by 2034

Source: Fact.MR





ECS-DoT Achieves Major Drone Performance Gains





Building Commercial Momentum

OEM Sales Network Buildout









Drones Hearables

What Market Is Responding To:

Ultra-low power AI inference

- < 10 ms vs. 150-300 ms for competitors
- Up to 20X lower energy consumption vs. competitors

AI Model Portability

- Supports a wide range of Al Models
- Fast and easy development cycle
- Interoperability

Multi Sensor Integration

- Seamless integration with a variety of sensor types
- Real-time fusion of audio, motion and environmental data

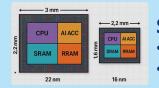


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Upcoming Catalysts

16nm Tape-out projected in Q4



Smaller with higher performance

- ~200% smaller and 1.3x less power
- Our highest performing ECS-DoT



Increased AI Performance

- Dynamic 1-16 bit precision with less power
- Increased AI model support

Ongoing OEM engagement for 22nm technology



New Evaluation Board

- **800%** smaller and easier to use
- Allows for convenient customers testing



What is resonating with OEMs

- Up to **20x** lower energy consumption
- Smaller batteries and sleeker product design

Live drone benchmarking



Transition to live drone trails

 Trails aim to validate the 50%+ endurance gains during simulation



Drone Market Validation

 Allows EMASS to increase partnerships and sales in the Drone Market



Thank You



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